Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1543	pipeline fetch decode count increment	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L2	742	L1 (control adj signal)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L3	123	L2 stipulated	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L4	123	L3 context	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L5	123	L4 processor	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L6	123	L5 instruction	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L7	0	L6 (program adj count)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L8	123	L6 PC	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L9	0	L8 thread	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L10	123	L6 PC	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L11	2578	pipeline fetch decod\$3 increment	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02

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L12	125	L11 stipulat\$3 context processor	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L13	1543	pipeline fetch decode count increment	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L14	742	L13 (control adj signal)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L15	123	L14 stipulated	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L16	123	L15 context	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L17	123	L16 processor	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L18	123	L17 instruction	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L19	0	L18 (program adj count)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L20	123	L18 PC	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L21	0	L20 thread	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L22	2578	pipeline fetch decod\$3 increment	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L23	123	L15 context	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02

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L24	123	L18 PC	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L25	125	L22 stipulat\$3 context processor	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L26	1	L25 (prefetch adj instruction\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L27	1543	pipeline fetch decode count increment	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L28	742	L27 (control adj signal)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L29	123	L28 stipulated	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L30	123	L29 context	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L31	123	L30 processor	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L32	123	L31 instruction	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L33		L32 (program adj count)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L34	123	L32 PC	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L35	0	L34 thread	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02

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L36	2578	pipeline fetch decod\$3 increment	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L37	1543	pipeline fetch decode count increment	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L38	742	L37 (control adj signal)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L39	123	L38 stipulated	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L40	123	L39 context	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L41	123	L40 processor	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L42	123	L41 instruction	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L43	0	L42 (program adj count)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L44	123	L42 PC	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L45	0	L44 thread	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L46	2578	pipeline fetch decod\$3 increment	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L47	125	L46 stipulat\$3 context processor	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02

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L48	1	L47 (prefetch adj instruction\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L49	123	L29 context	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L50	123	L32 PC	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L51	125	L36 stipulat\$3 context processor	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L52	123	L39 context	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L53	123	L42 PC	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L54	125	L46 stipulat\$3 context processor	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L55	1	(two near5 ((delay adj units)) near5 (clock adj cycle))	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L56	23	((delay adj units)) near5 (clock adj cycle)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L57	10	((delay adj units)) near5 (clock adj cycle) series	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L58	7	((delay adj units)) near5 (clock adj cycle) (series near3 connect\$3)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02
L59	1543	pipeline fetch decode count increment	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2006/03/07 09:02

L60	742	L59 (control adj signal)	US-PGPUB; USPAT;	AND	OFF	2006/03/07 09:02
	_		USOCR; IBM_TDB			

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